A True Positives Theorem for a Static Race Detector

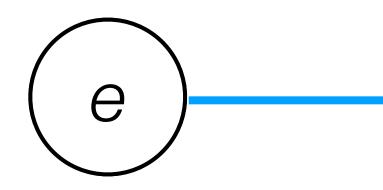
Presentation by Julia Belyakova and Artem Pelenitsyn For <u>CS 7580</u> (instructor: Jan Vitek), 10/30/2019

A subset of slides is taken from <u>Ilya Sergey's web page</u>

Static Analyses for Program Validation

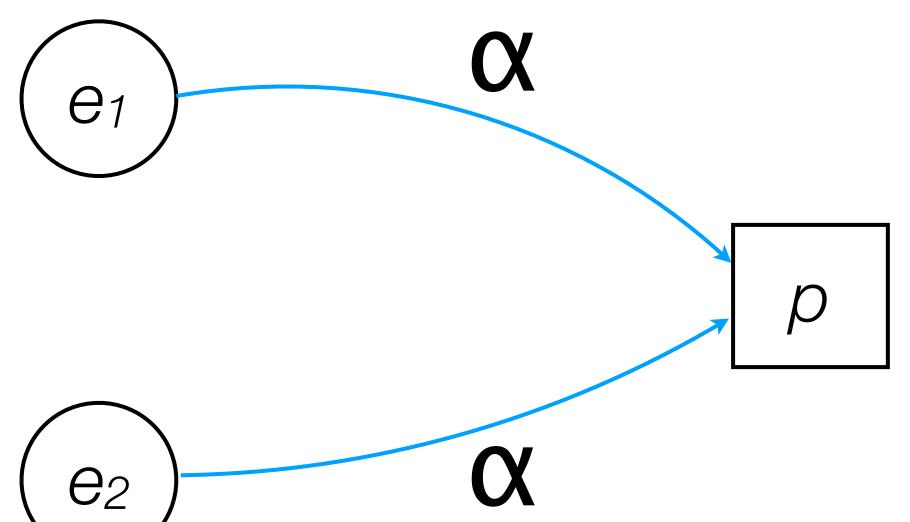
The Essence of Static Analysis

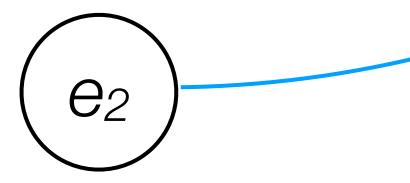
"abstraction"

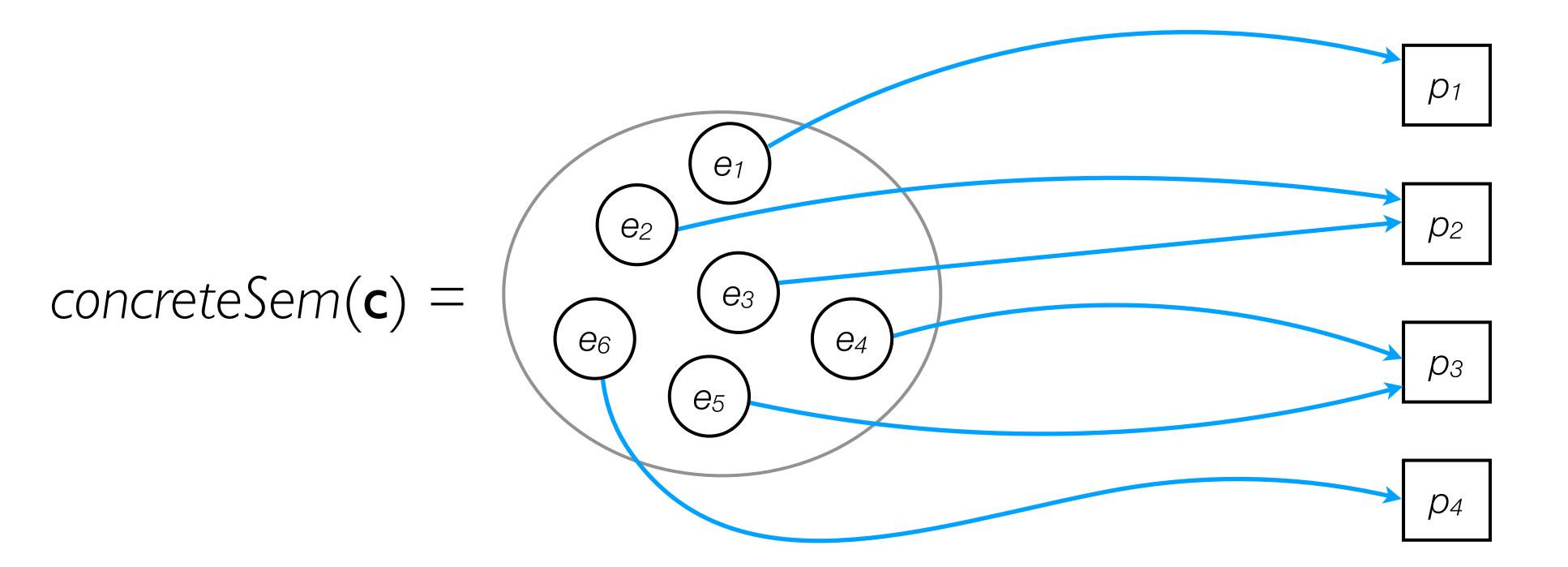


program execution **X** *p*

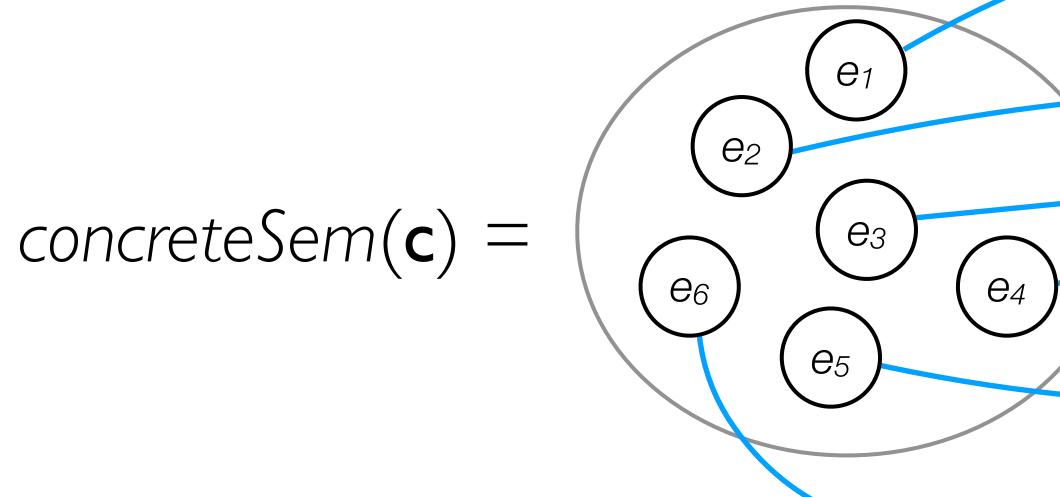
property of interest







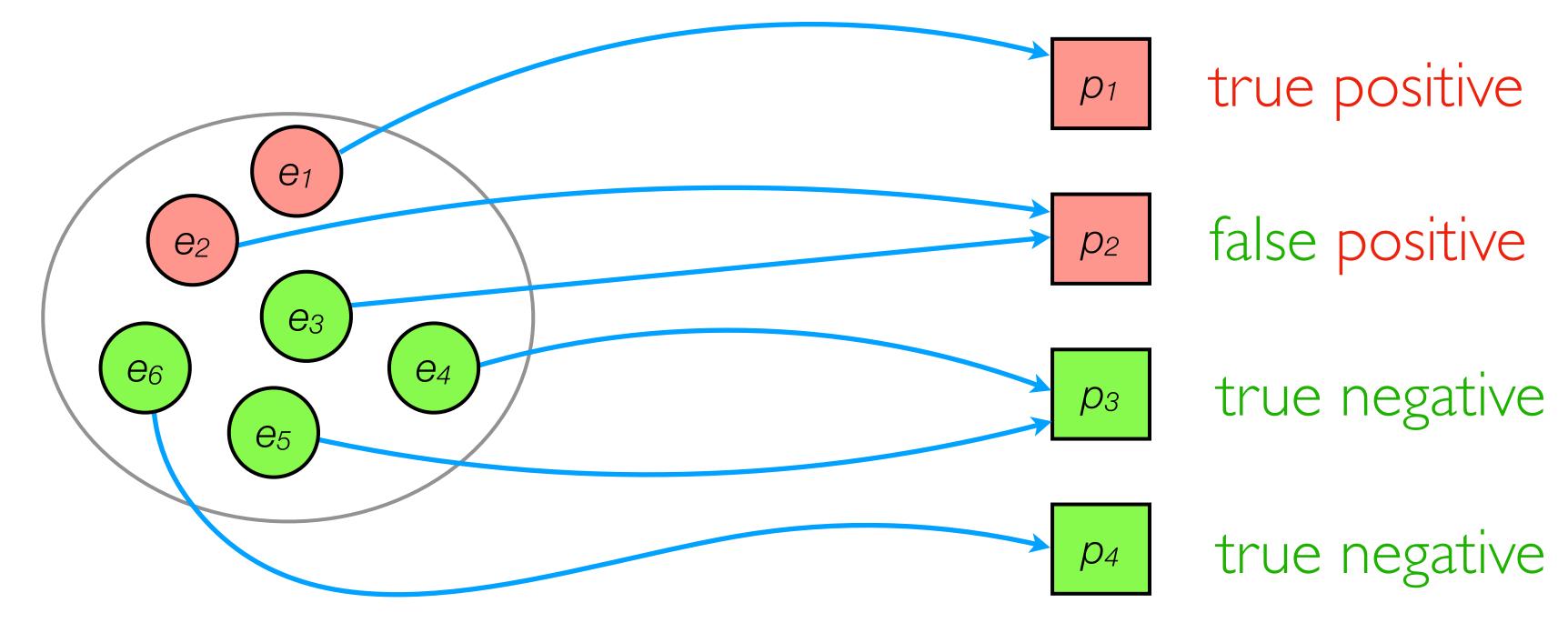
Static Analysis

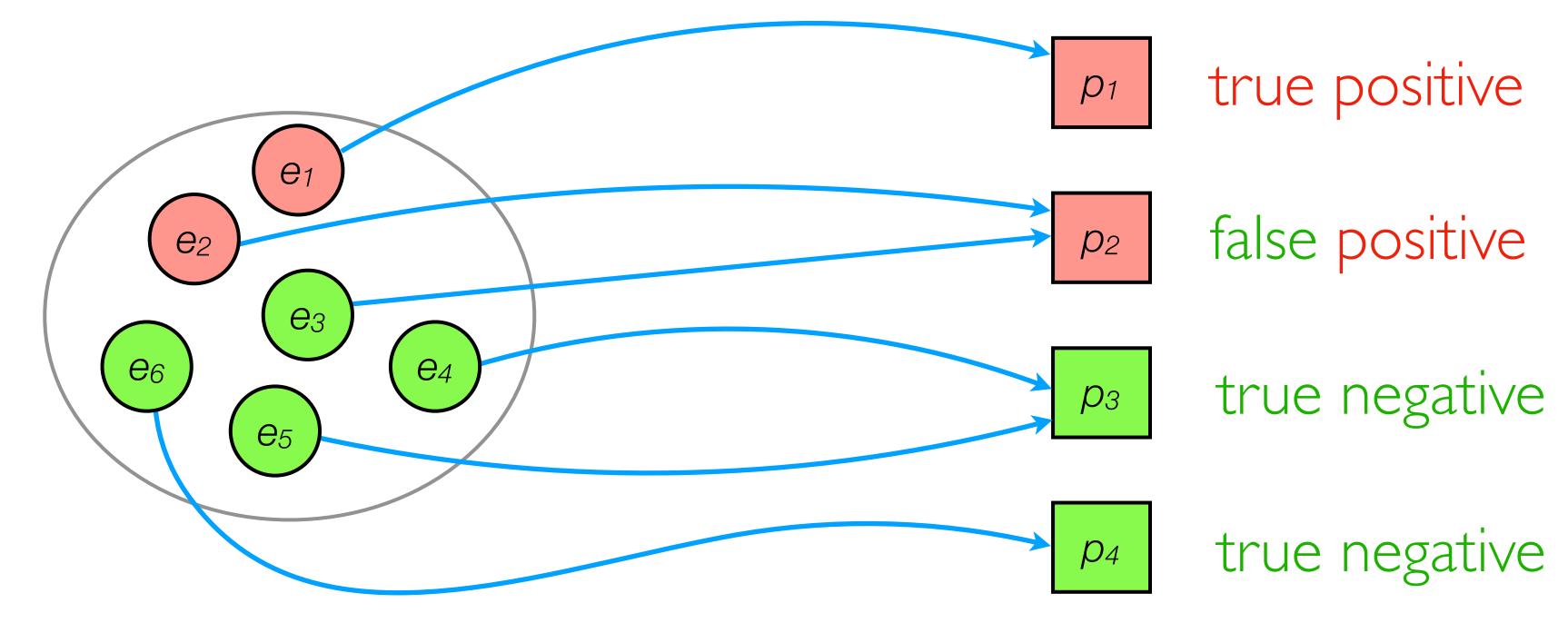


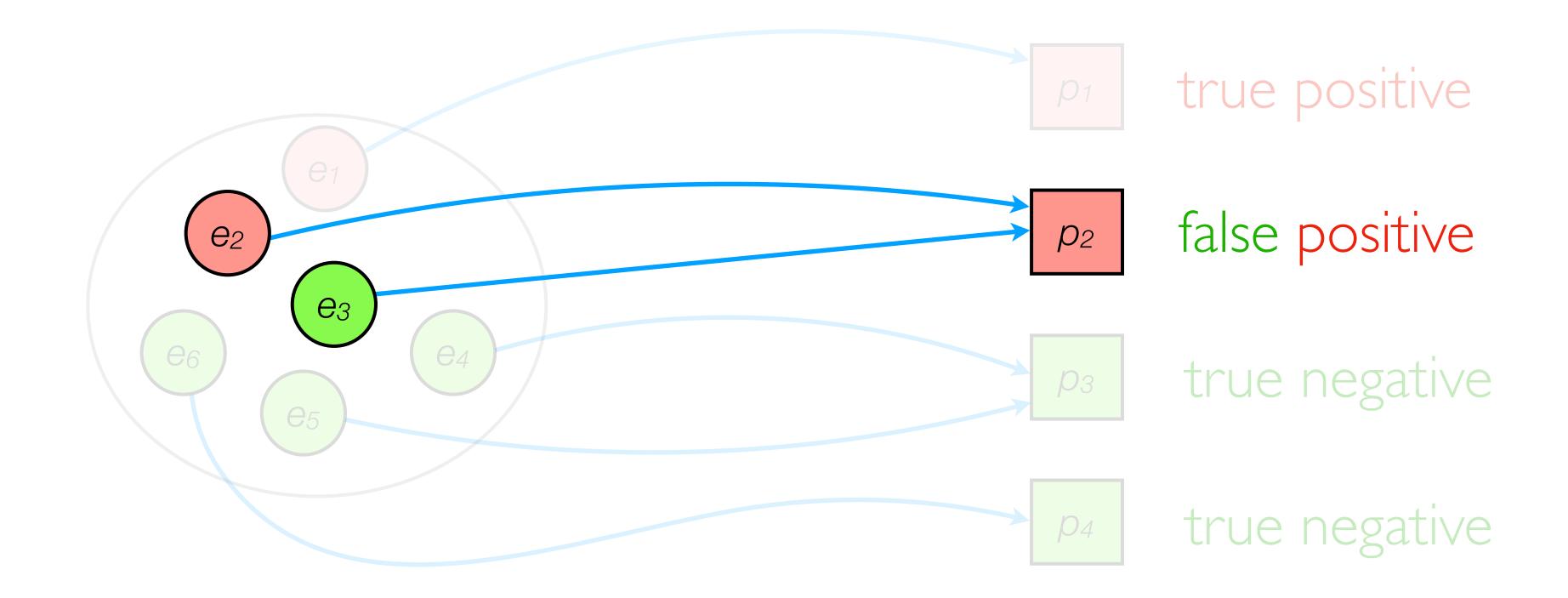
Static Analysis *p*₁ "has bugs" p_2 **p**3 "correct" p_4

Verifier Or a Bug Detector?

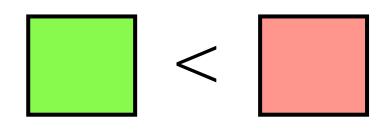
Program Verifier



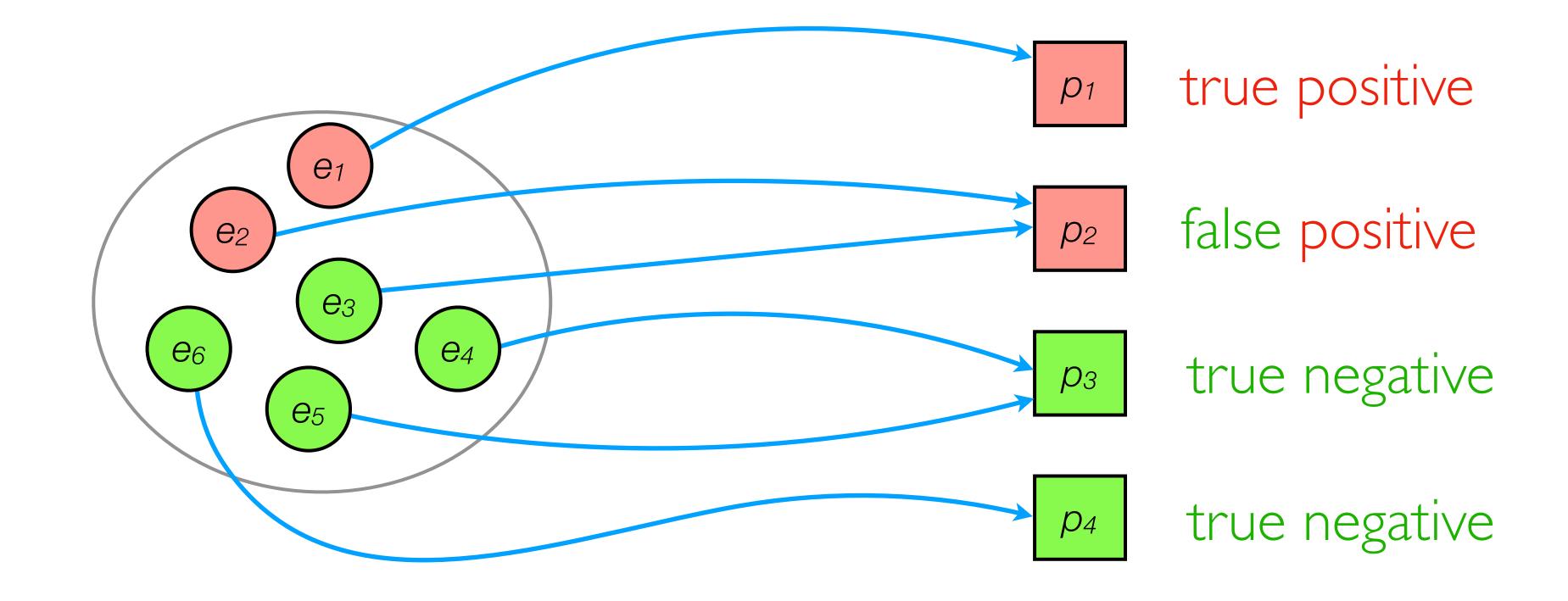




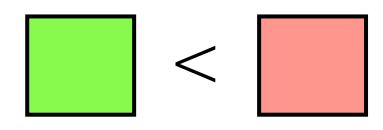
abstract over-approximation



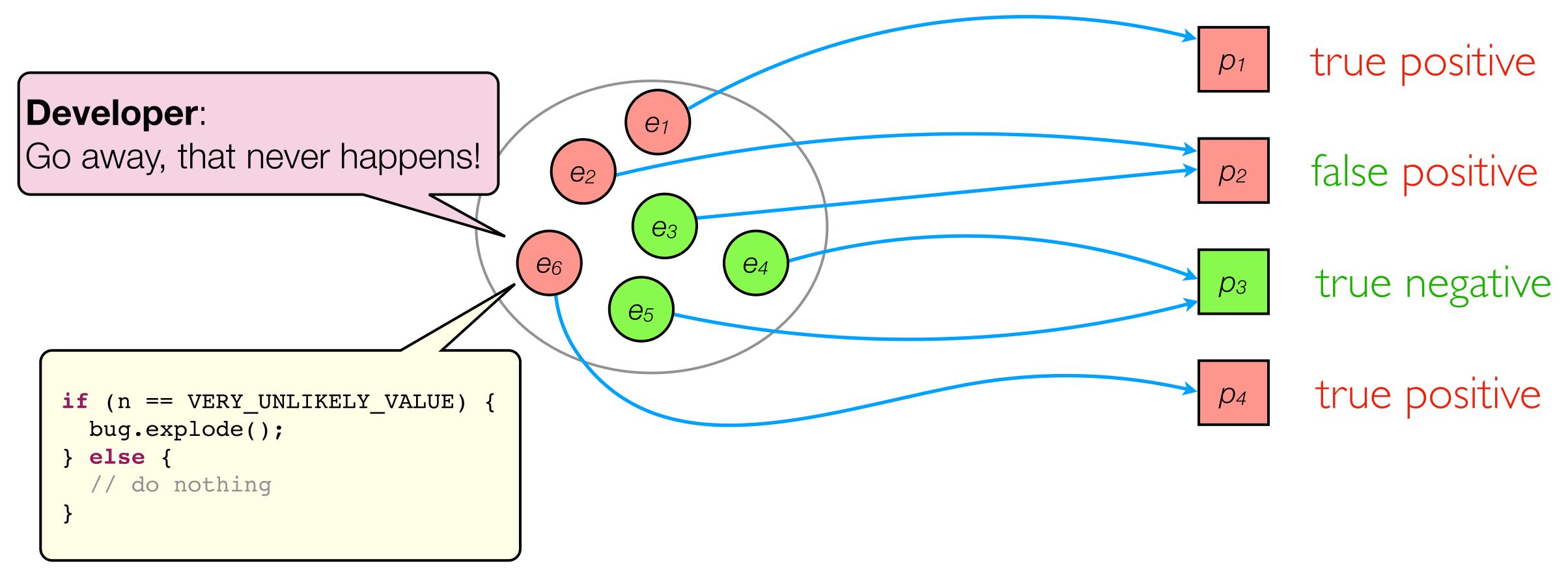




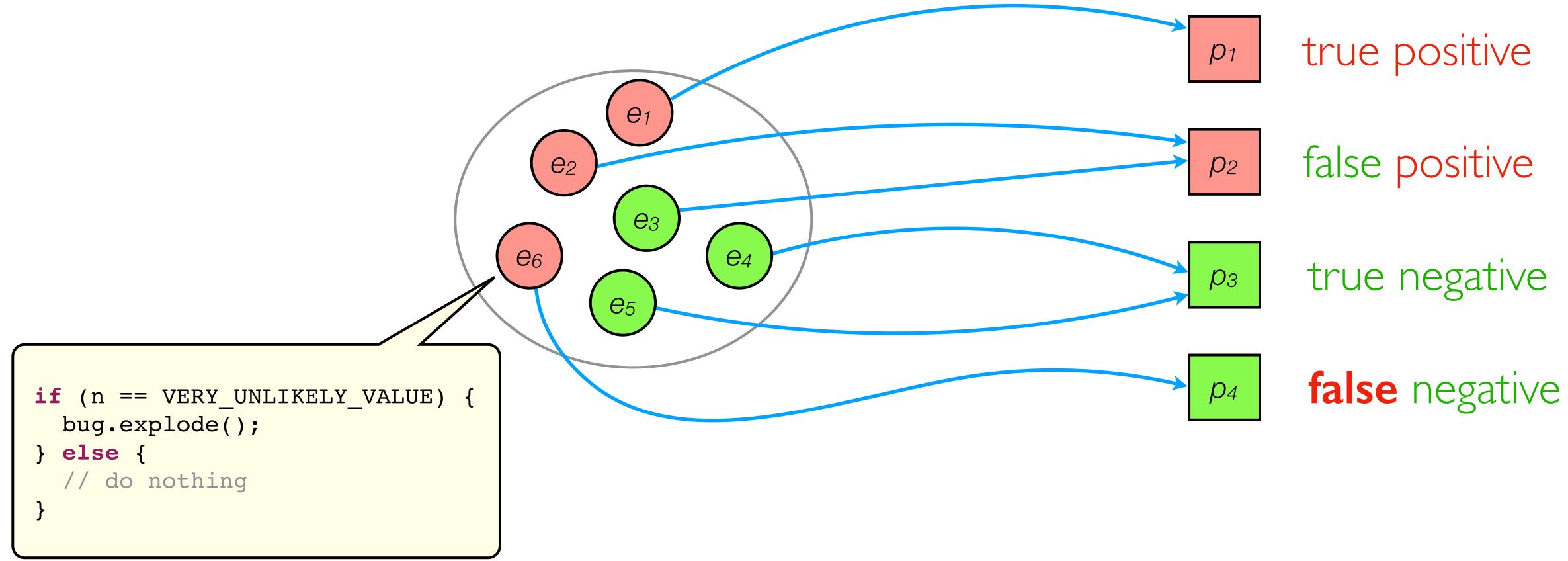
abstract over-approximation

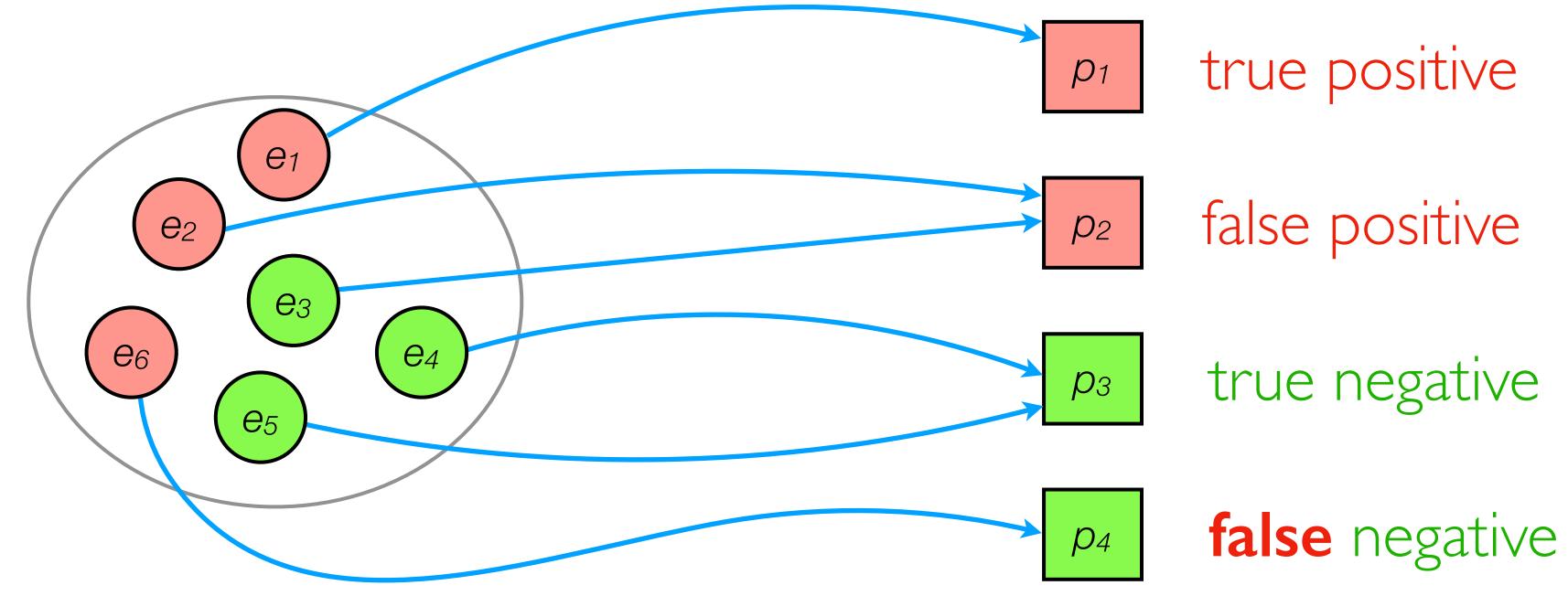


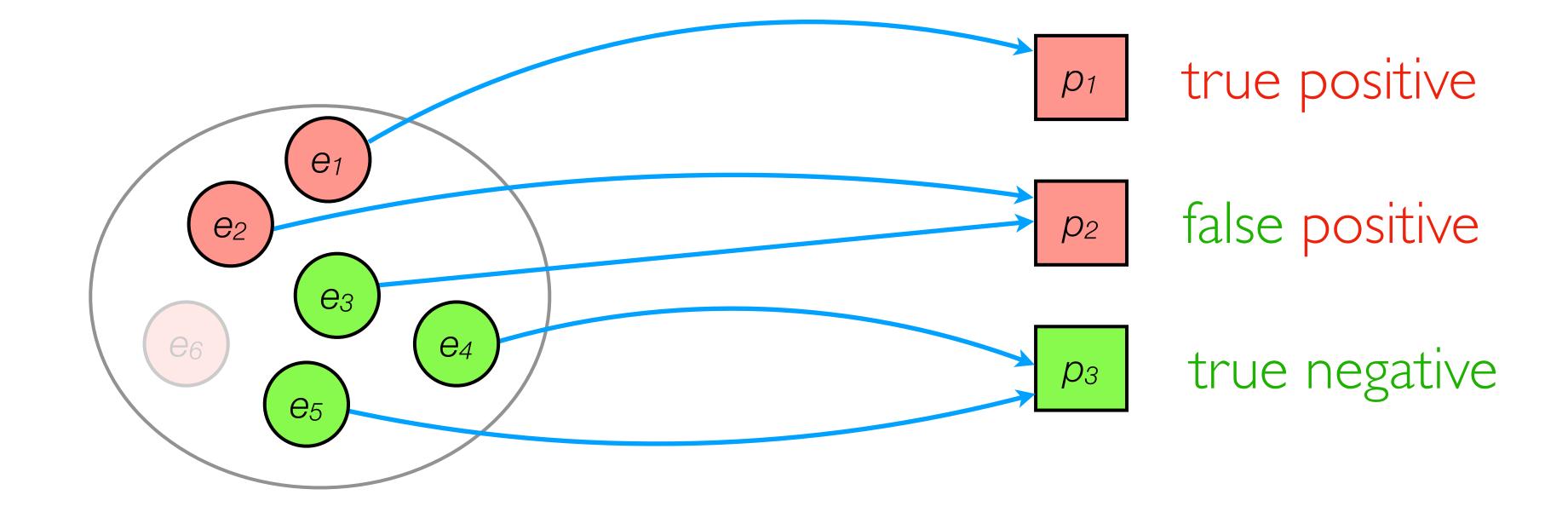




Unsound Program "Verifier"

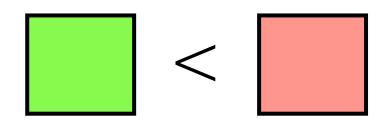






concrete under-approximation

abstract over-approximation

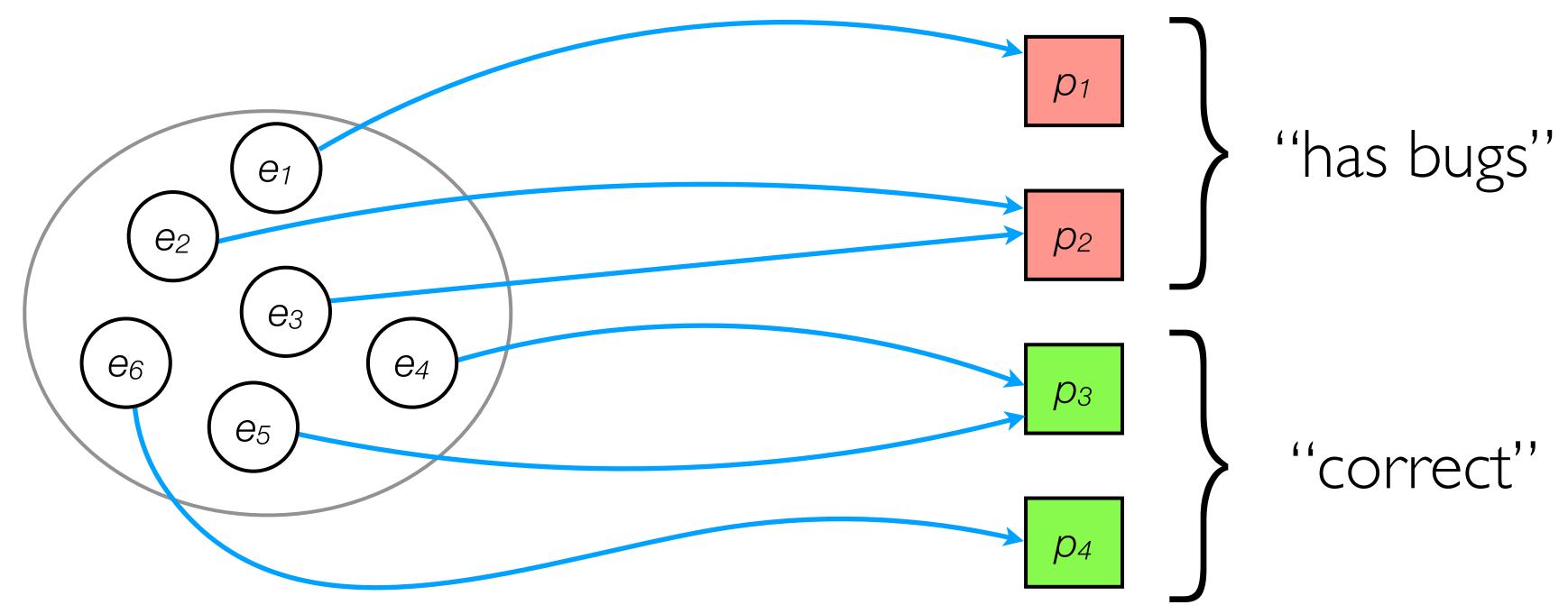




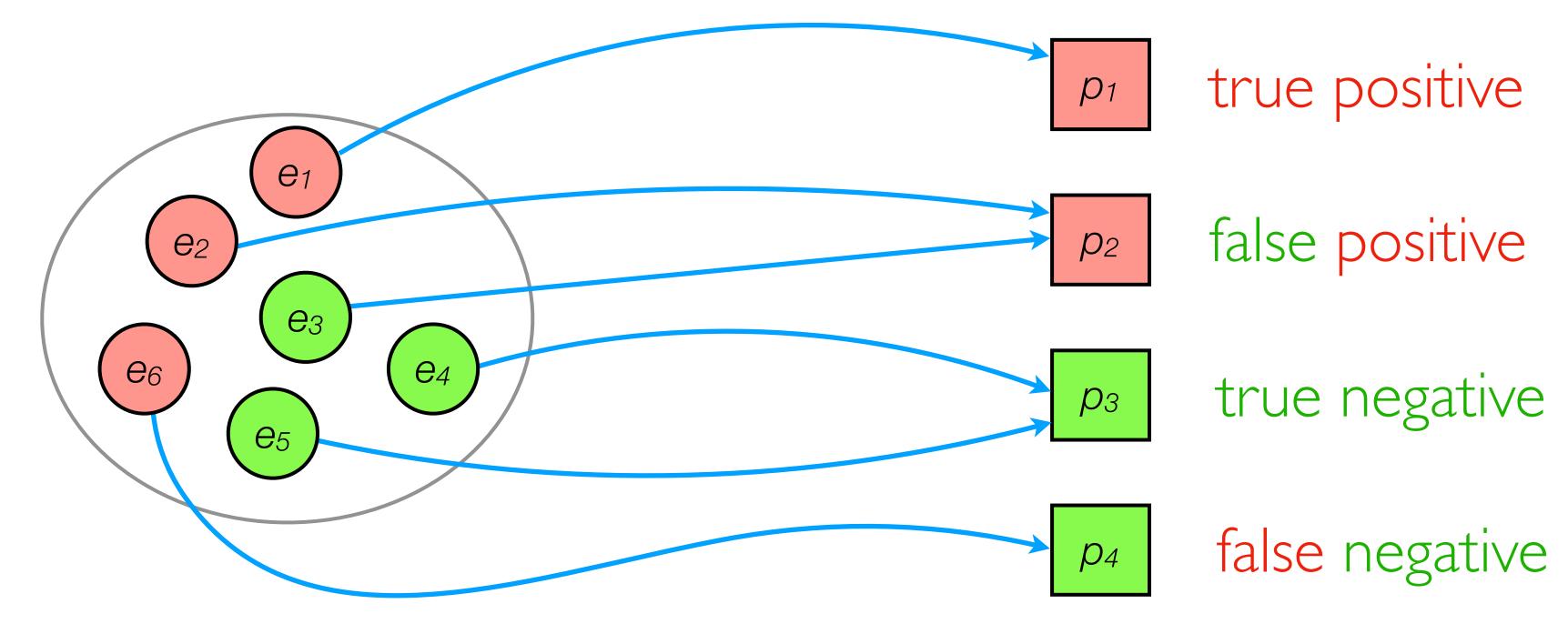
Sound Static Verifiers

- False negatives (bugs missed) are **bad**
- False positives (non-bugs reported) are okay
- Constructed as over-approximation (of under-approximation)
- Soundness Theorem:

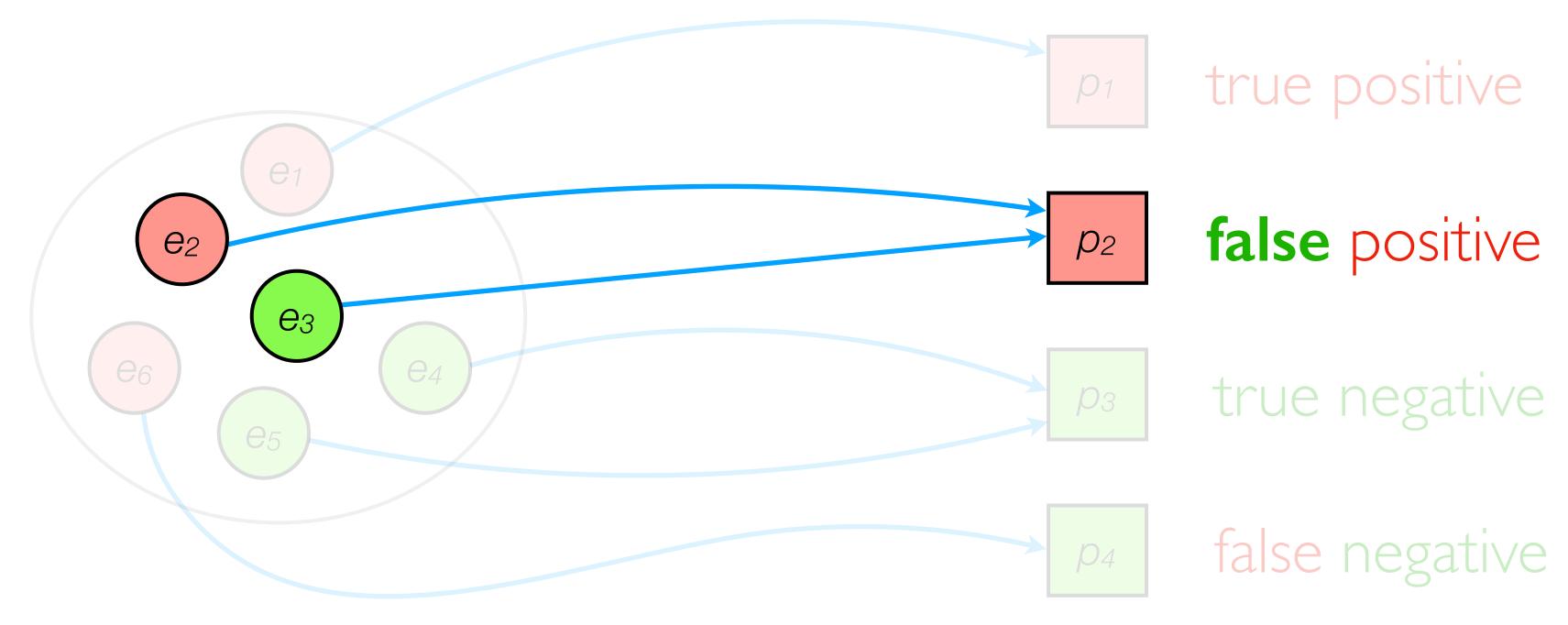
Under certain assumptions about the programs, the analyser has no false negatives.



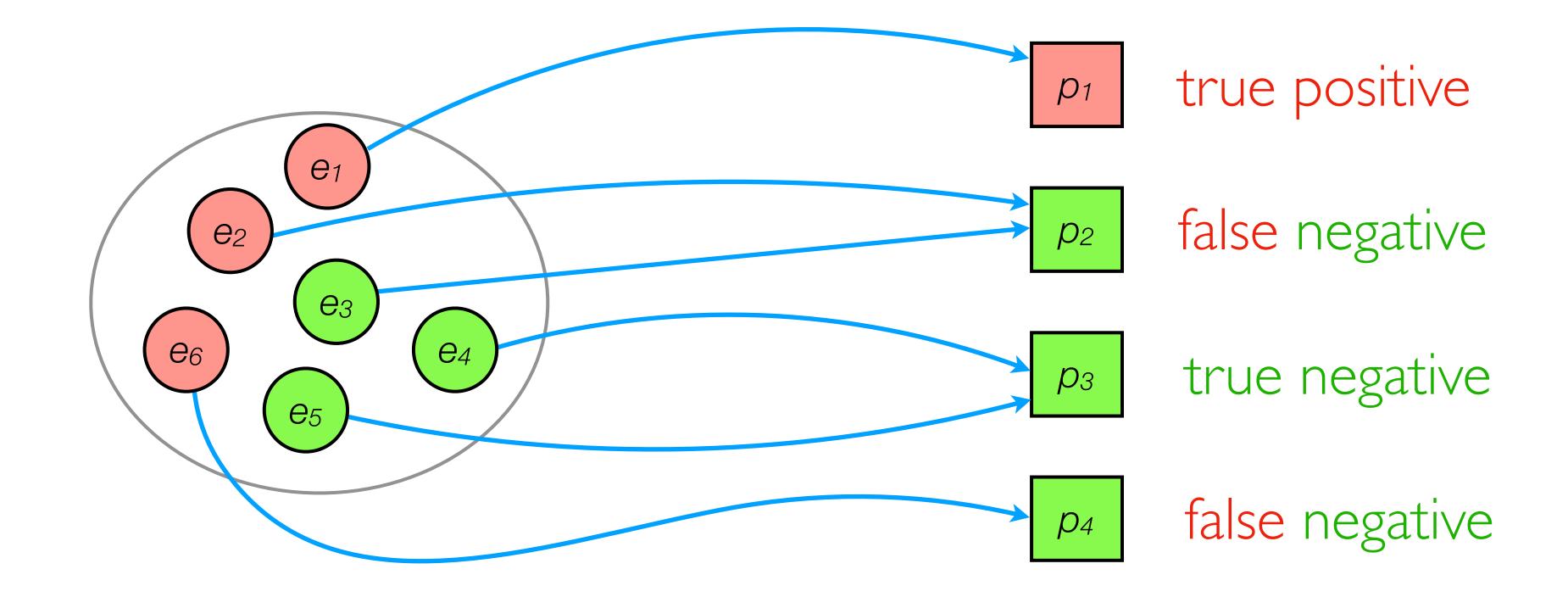
Static Bug Finder



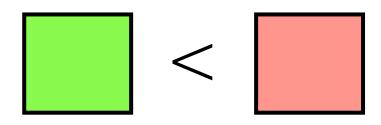
Unsound Static Bug Finder



Sound (but imprecise) Static Bug Finder

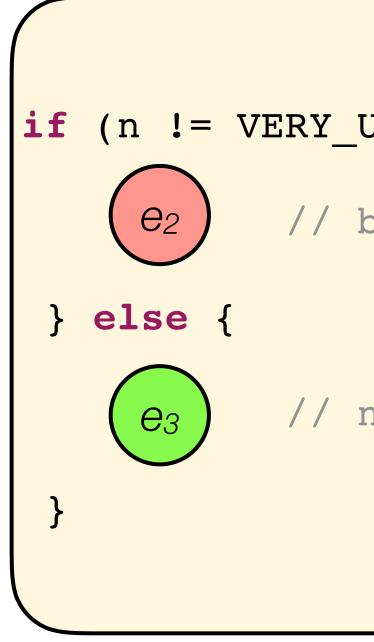


abstract under-approximation





Loss of Precision in Static Bug Finders



Idea: over-approximate in concrete semantics!

(n != VERY_UNLIKELY_VALUE) {

bug happens here

normal execution



Sound (but Imprecise) Static Bug Finder

e1

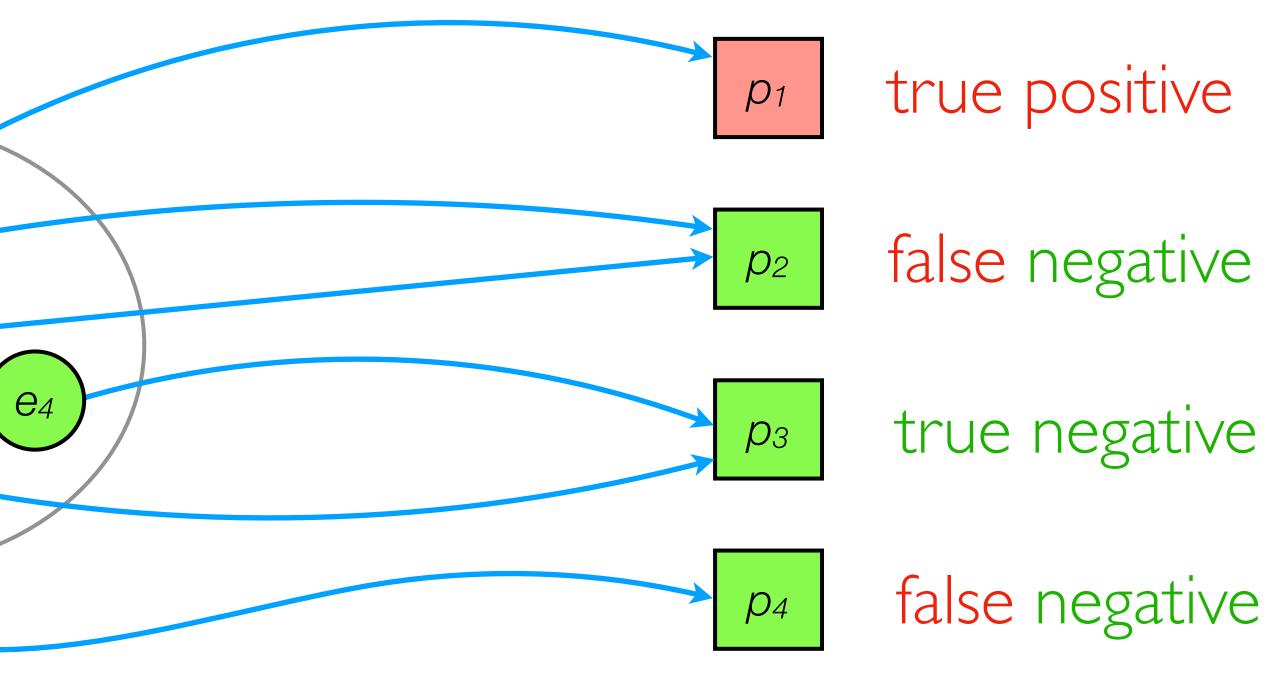
Өз

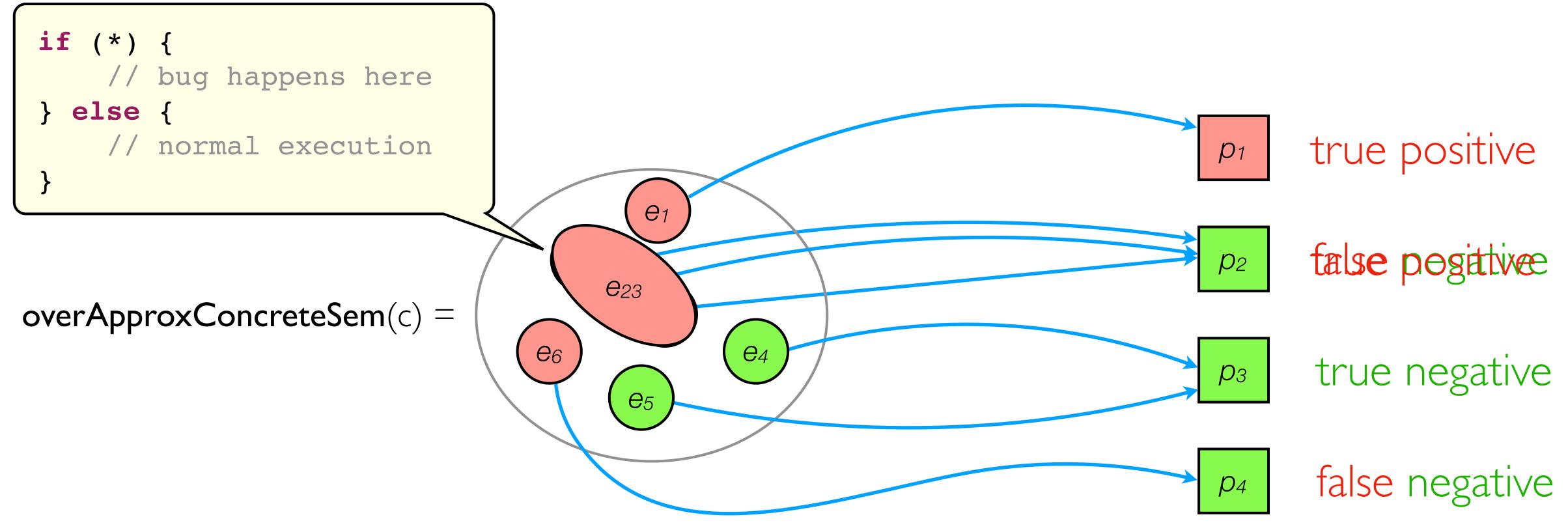
e5

e2

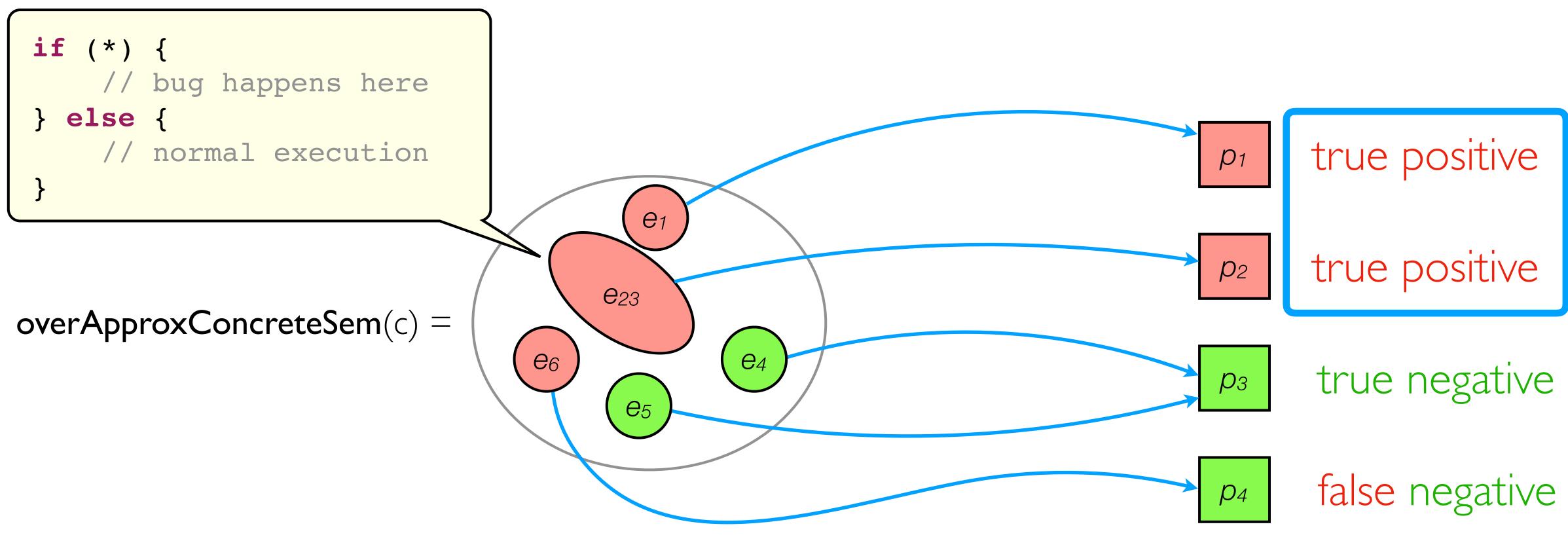
e6

Let's merge these executions into one that subsumes both!



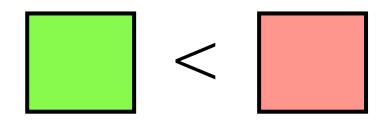


Sound Static Bug Finder



concrete over-approximation

abstract under-approximation





- False negatives (bugs missed) are okay
- False positives (non-bugs reported) are **bad**
- Constructed as under-approximation of over-approximation
- Soundness (True Positives) Theorem:

Towards Sound Static Bug Finders (this work)

Under certain assumptions about the programs, the analyser has **no false positives**.



A True Positives Theorem for a Static Race Detector

Nikos Gorogiannis

facebook











Peter O'Hearn

Ilya Sergey

facebook

YaleNUSCollege



Key Messages

Unsound (and incomplete) static analyses can be *principled*, satisfying meaningful theorems that help to understand their behaviour and guide their design

One can have an unsound but effective static analysis, which has significant industrial impact, and which is supported by a *meaningful theorem*.



Context

- 1. We had a demonstrably-effective industrial analysis:
- 2. No soundness theorem
- 3. Architecture: compositional abstract interpreter
- 4. No heuristic alarm filtering

Just ad hoc?

Our reaction:

RacerD (OOPSLA'18); >3k fixes in Facebook Java



Semantics/theory should understand/explain, not lecture.

Case Study: RacerDX

- A provably TP-Sound version of Facebook's RacerD concurrency analyser (Blackshear et al., OOPSLA'18)
- Buggy executions: data races in lock-based concurrent programs
- Syntactic assumptions: Java programs with well-scoped locking (synchronised), no recursion, reflection, dynamic class loading; global variables are ignored.
- Concrete over-approximation: Loops and conditionals are non-deterministic.

Formal Result

RacerDX enjoys the True Positives Theorem wrt. Data Race Detection

(Details in the paper)

Static Analysis with True Positives Theorem*

Goal: to build a static analysis s.t. if the analysis **reports a bug**, it is a **true bug**

For an Idealized Language

True bug can be exhibited

The race reported by the analysis for program P is a **true race**

There exists an execution of P that exhibits the race



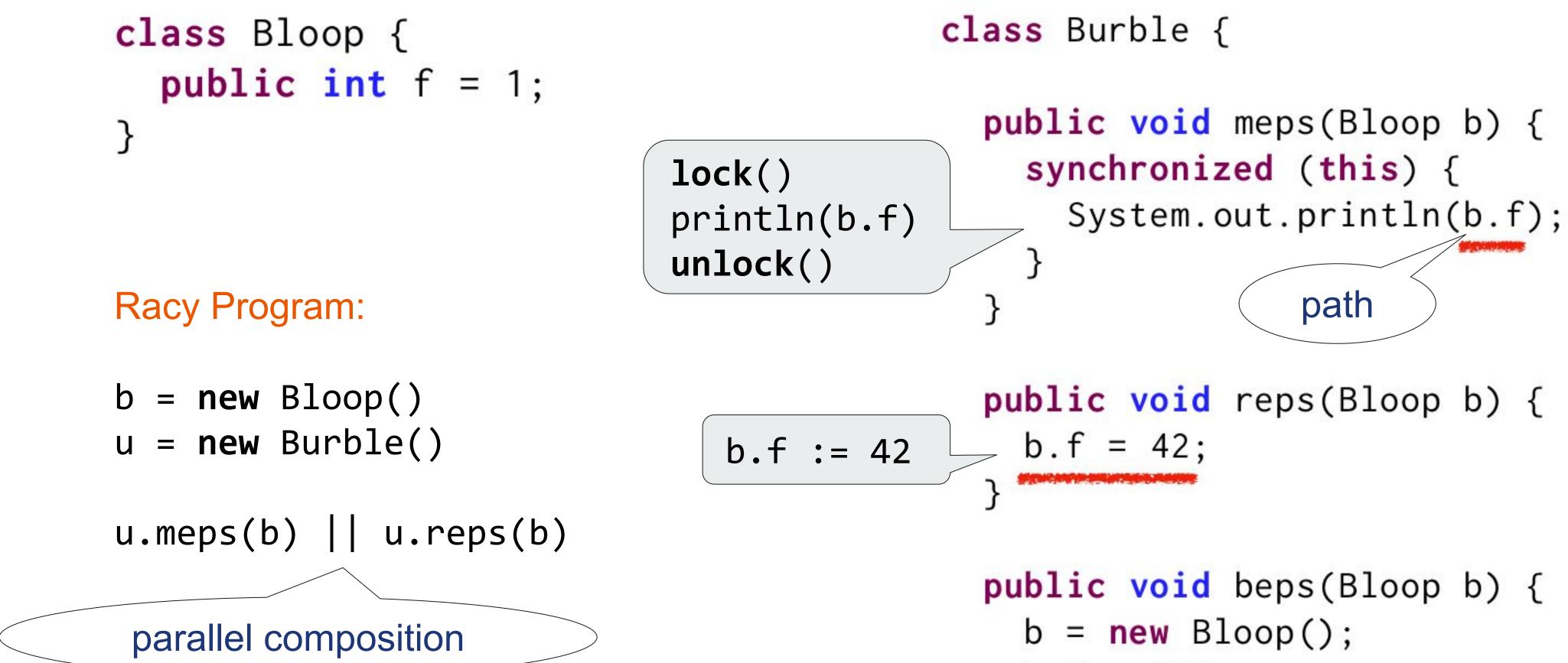
Ingredients of the formalism

For an Idealized Language

- program
- execution
- race
- analysis
- proof



Ingredients of a data race

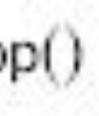


```
b.f = 239;
```



Concurrent program syntax

$f \in Field$ $x, \arg_i \in Var$		field names variables
$\pi \in Path$::=	$x.f \mid \pi.f$
e ∈ Exp		$Var \cup Path$
$c \in Stmt$::=	$skip \mid x := x \mid x := \pi$
$C \in CStmt$::=	$c \mid C; c \mid C; if * then$
$M \in Method$::=	$m(arg_1,\ldots,arg_n)$ { (
$p \in Program$::=	$C \parallel C$





Single-threaded program C: concrete semantics

• State $\varsigma = \langle c, S, h, L \rangle$ (command, stack, heap, locks)

• Trace (list of states)

$$\tau = [\varsigma_0, \ldots, \varsigma_n]$$

• Concrete semantics (set of traces) $\llbracket C
rbrace \varsigma \in \mathcal{P}(\mathcal{T})$





Concrete semantics of commands

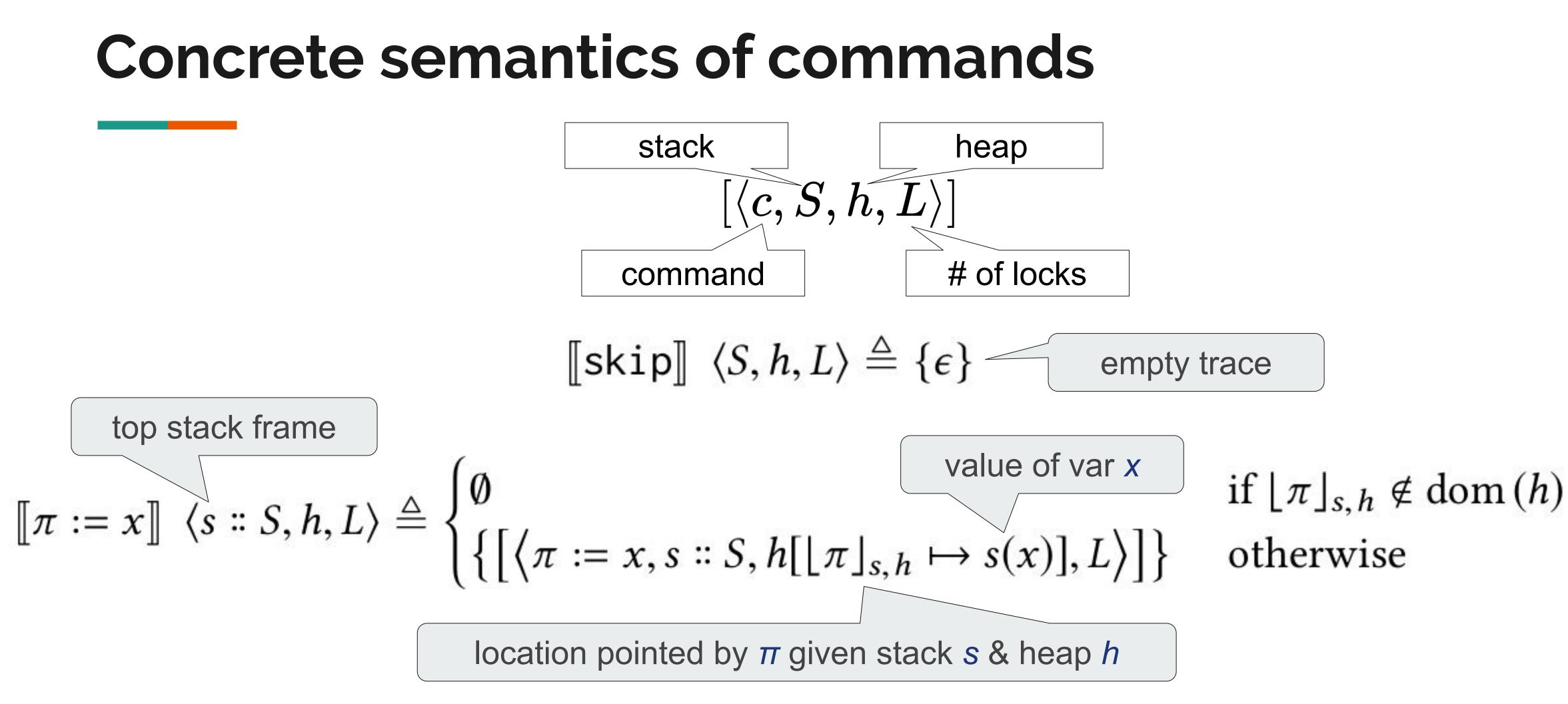
 $if [\pi]_{s,h} \notin dom (h)$ $[\pi]_{s,h})] :: S, h, L\rangle]$ otherwise $if [\pi]_{s,h} \notin dom (h)$ $[]_{s,h} \mapsto s(x)], L\rangle]$ otherwise $[L\rangle] \left| \begin{array}{c} \ell \notin locn(h), s' = s[x \mapsto \ell], \\ h' = h \cup \bigcup_{f \in Field} \{(\ell, f) \mapsto \ell\} \end{array} \right|$ $: S, h, L\rangle]$

 $if L \le 0$ 1)]} otherwise





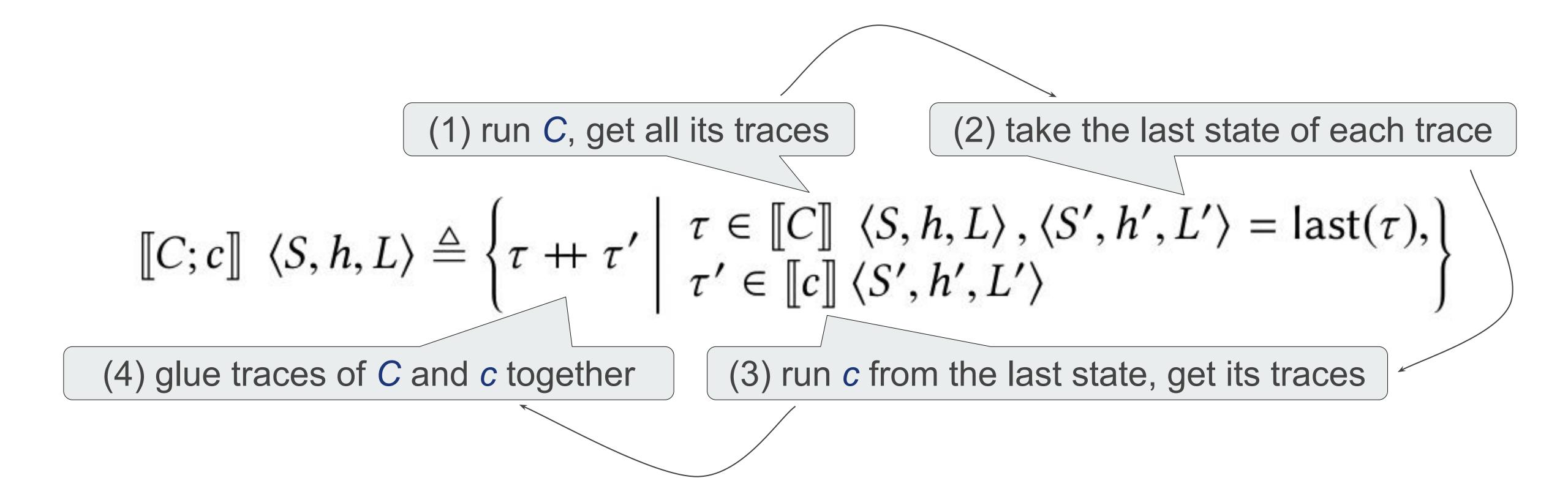




 $[\operatorname{lock}()] \langle S, h, L \rangle \triangleq \{ [\langle \operatorname{lock}(), S, h, \operatorname{add}(L, 1) \rangle] \}$

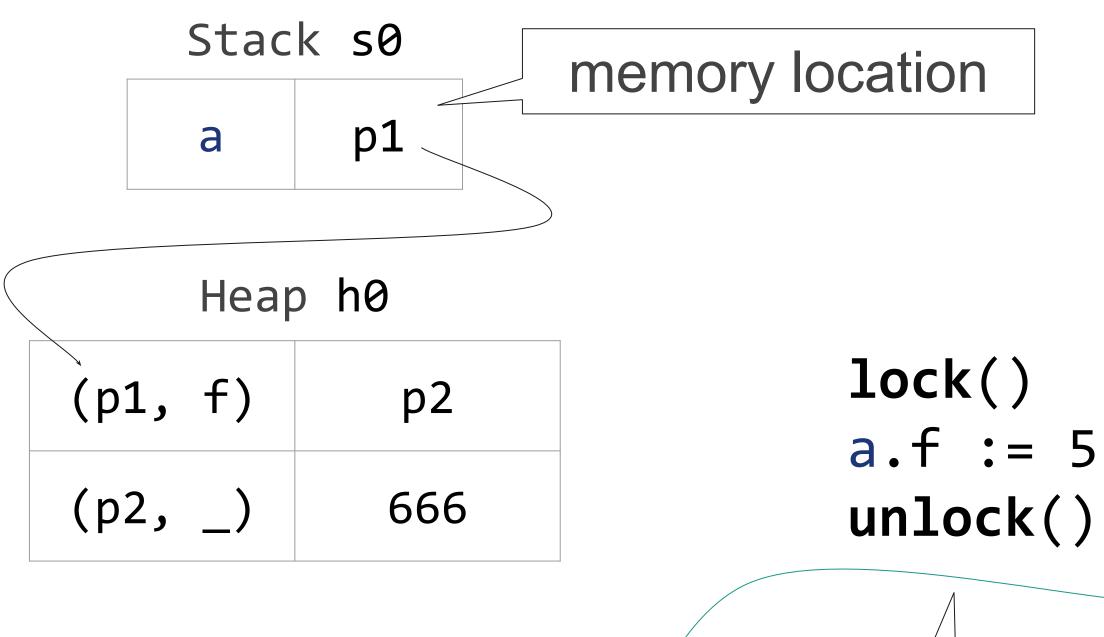


Concrete semantics of compound statements

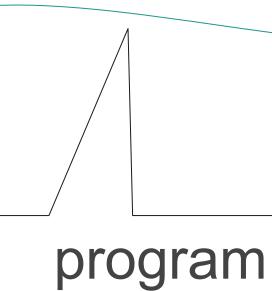


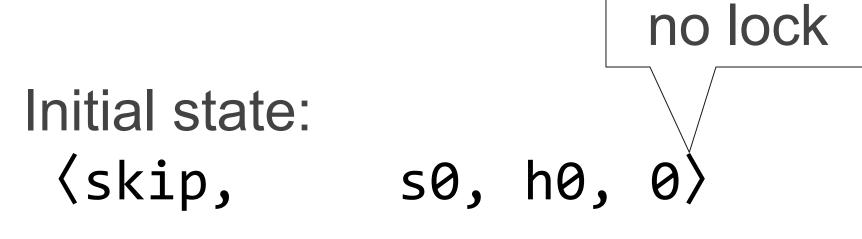


Concrete trace example



Неар	o h1
(p1, f)	p2
(p2, _)	5





 $\langle lock(), s0, h0, 1 \rangle$, ⟨a.f:=5, s0, h1, 1⟩, $\langle unlock(), s0, h1, 0 \rangle$ execution trace



Two-threaded program C₁IIC₂: concrete semantics

State $\langle c_{\parallel}, (S_1, S_2), h, (L_1, L_2) \rangle$ **CIIE OL EIIC**

• Trace $\tau^{||} = [\varsigma_0^{||}, \dots, \varsigma_n^{||}]$

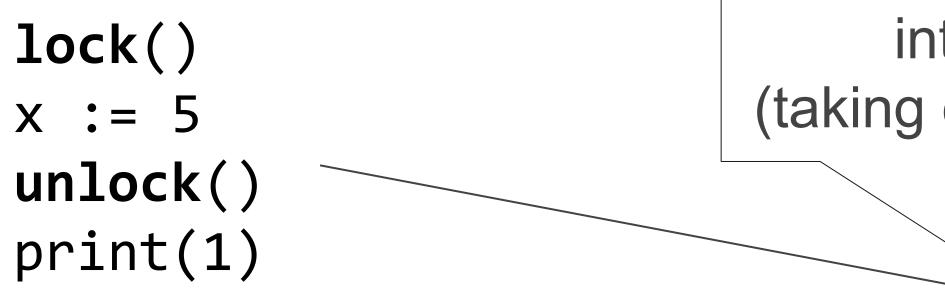
• Concrete semantics $\llbracket C_1 \parallel C_2 \rrbracket \varsigma^{\parallel} \in \mathcal{P}(\mathcal{T}^{\parallel})$

2-threaded program interleaves single traces

 $\tau_1 \in \llbracket C_1 \rrbracket \langle S_1, h, L_1 \rangle$ $\tau_2 \in [C_2] \langle S_2, h, L_2 \rangle$ (1) run components individually

 $[C_1 || C_2] \langle (S_1, S_2), h, (L_1, L_2) \rangle$ interl $\tau_1 \tau_2 \langle (S_1, S_2), h, (L_1, L_2) \rangle$ (2) interleave all individual traces (full and partial)

Concurrent traces example



lock()
x := 5
unlock()
lock()
y := 777
unlock()
print(1)

interleave
(taking care of locks)

ck() lock()
:= 5 y := 777
lock() unlock()
ck() print(2)

lock()

. . .

lock()
x := 777
unlock()
print(2)

. . .



Data race means concurrent access to location

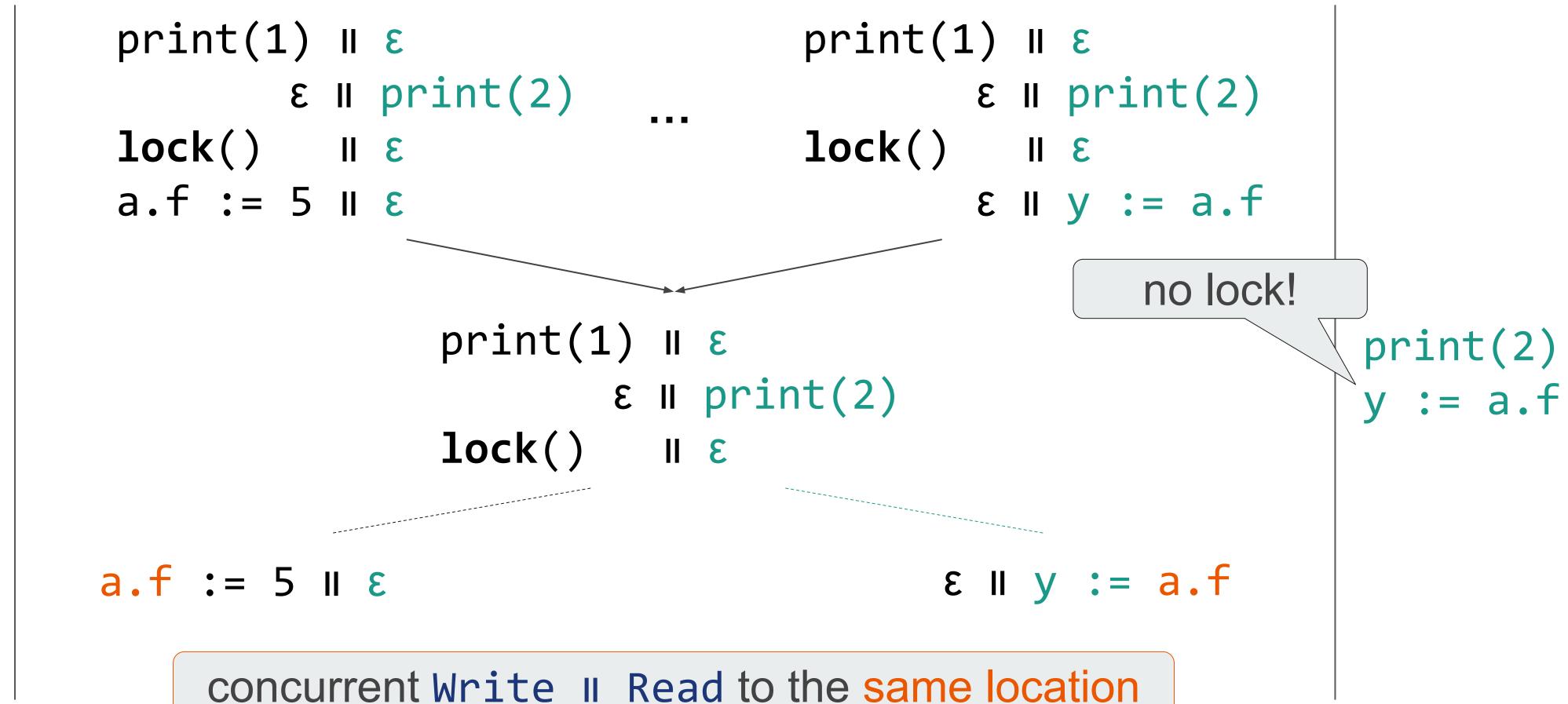
Definition 3.6 (Data Race). The program $C_1 \parallel C_2$ races if there exists a state ς_0 and a non-empty concurrent trace $\tau \in \llbracket C_1 \parallel C_2 \rrbracket \varsigma_0$ such that $last(\tau) = \langle (s_1 \parallel \ldots, s_2 \parallel \ldots), h, \ldots \rangle^9$ and, • there exist paths π_1, π_2 such that $\lfloor \pi_1 \rfloor_{s_1,h} = \lfloor \pi_2 \rfloor_{s_2,h}$; • there exist states $\varsigma_1 = \langle c_1 \parallel \epsilon, _, _, _\rangle$ and $\varsigma_2 = \langle \epsilon \parallel c_2, _, _, _\rangle$ such that $\tau :: \varsigma_1, \tau :: \varsigma_2 \in \llbracket C_1 \parallel C_2 \rrbracket \varsigma_0$; • $c_1 = (\pi_1 := _) \land c_2 = (\pi_2 := _), \text{ or, } c_1 = (\pi_1 := _) \land c_2 = (_ := \pi_2), \text{ or, } c_1 = (_ := \pi_1) \land c_2 = (\pi_2 := _).$





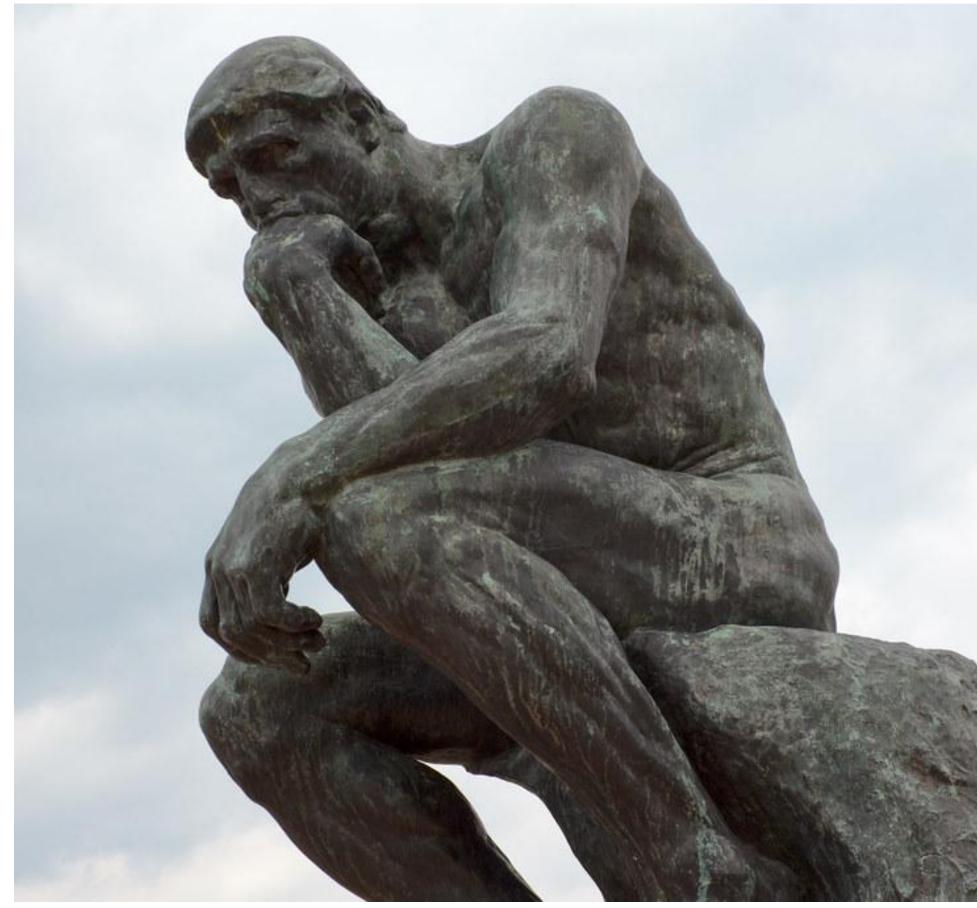


Data race means concurrent access to location





Can we identify a data race *without* building the traces?





Abstract Semantics tracks accesses to memory locations helps identify true races

- Abstract State $\langle W, L, A \rangle$ (wobblies, locks, path accesses)
- Abstraction of a set of concrete *single*-threaded traces $\alpha(T) = \langle W, L, A \rangle$

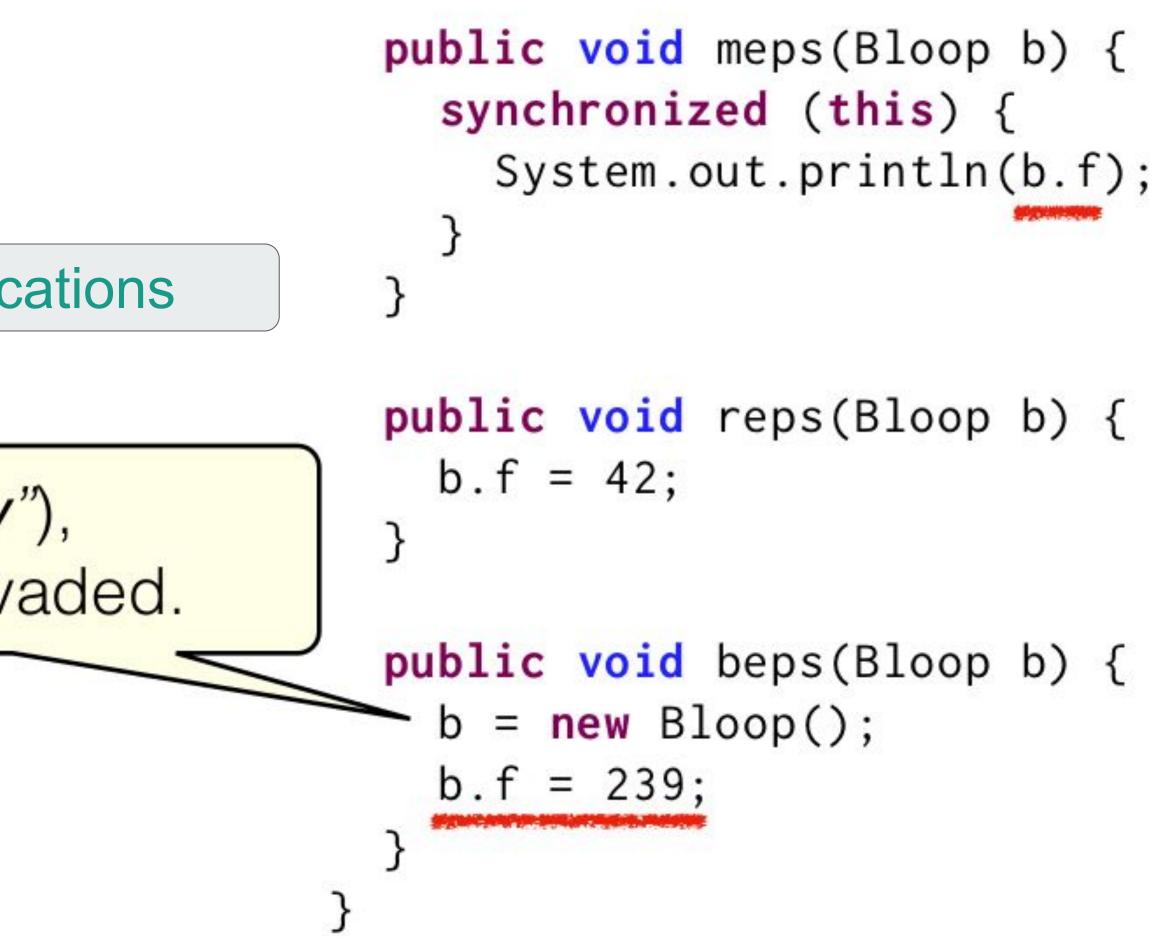


Wobblies can evade data races (produce false positives)

class Bloop { public int f = 1; }

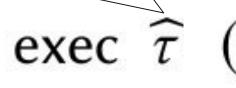
same path b.f refers to different locations

Path prefix b is "unstable" ("wobbly"), as it's reassigned, hence race is evaded. **class** Burble {

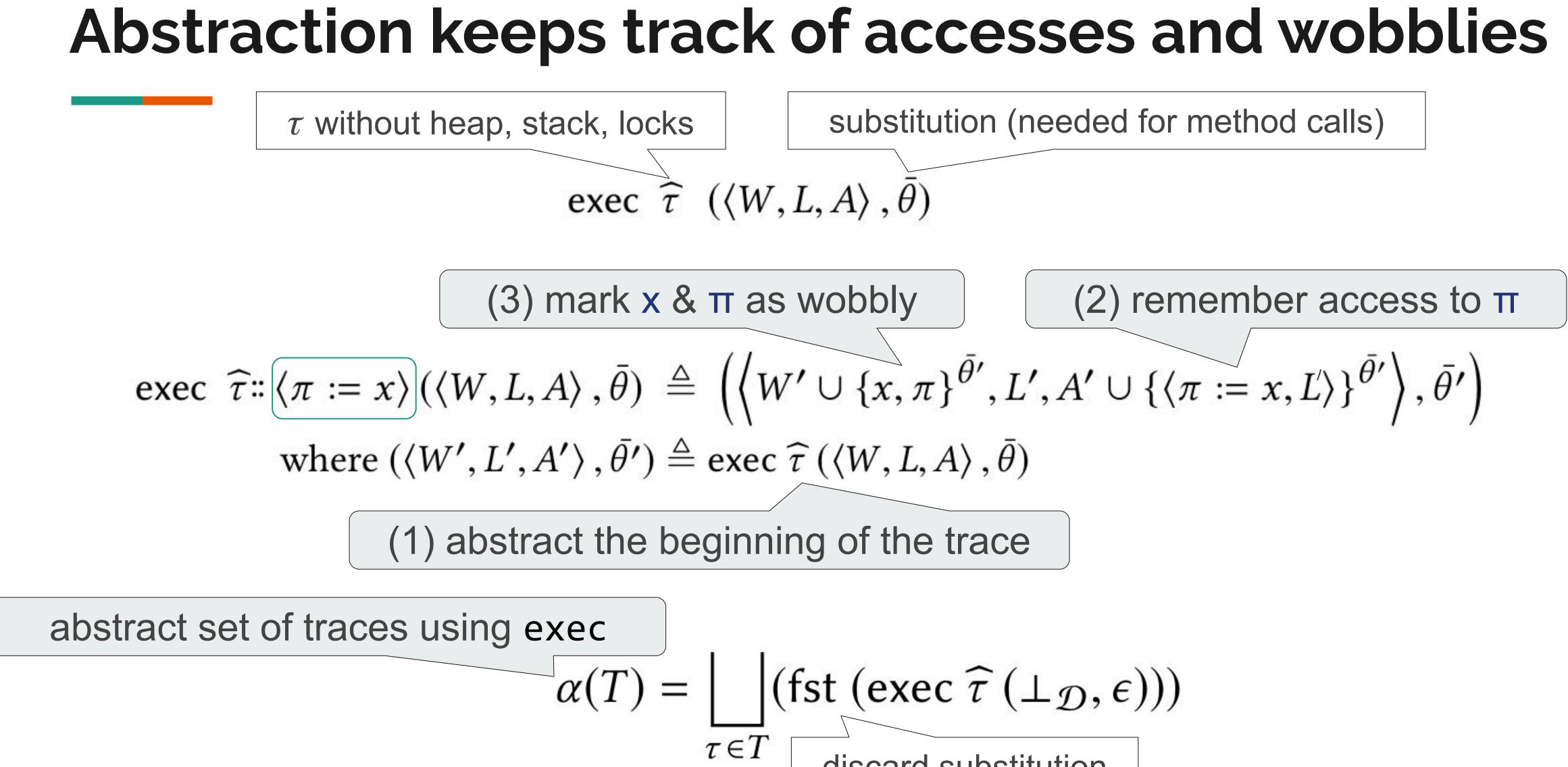




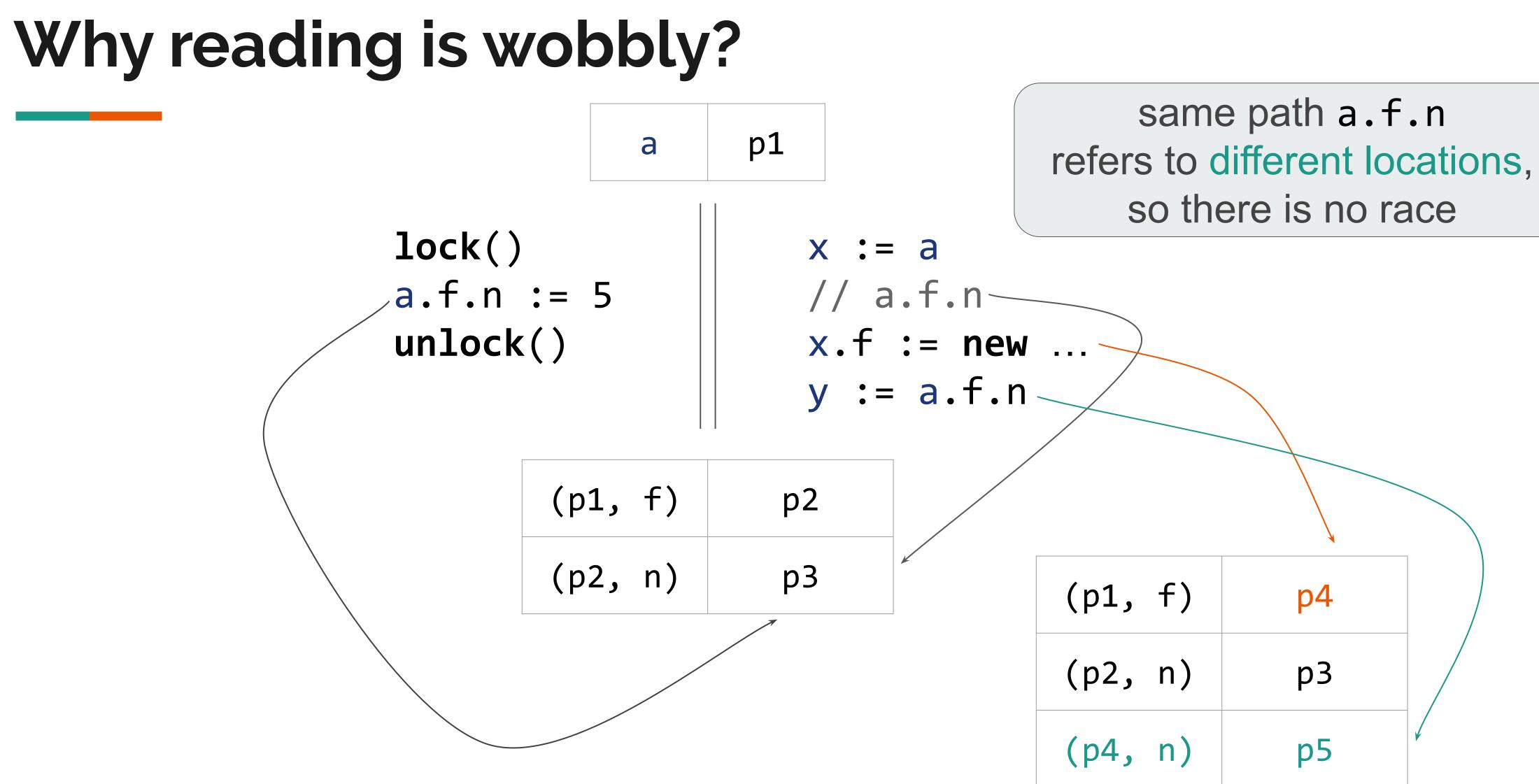


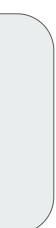


exec
$$\widehat{\tau}$$
:: $\langle \pi := x \rangle (\langle W, L, A \rangle, \overline{\theta}) \triangleq$



discard substitution







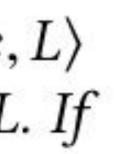
Abstract access captures concrete access

LEMMA 5.6 (PATH ACCESS EXISTENCE). Let T be a set of traces, $\alpha(T) = \langle _, _, A \rangle$, and $q = \langle c, L \rangle$ (where $c = (x := \pi)$ or $c = (\pi := x)$) is a query about the access path π in the locking context L. If $q \in A$ then there exist a trace $\tau \in T$ and a non-empty, shortest prefix $\tau' \preccurlyeq \tau$ such that • the last state of τ' is $\langle c', _, _, L \rangle$ and c', c are both stores or loads;

- exec $\widehat{\tau'}(\perp_{\mathcal{D}}, \epsilon) = (\langle _, _, A \rangle, \overline{\theta})$ where $\pi \in A$;
- a path π' such that $c' = (x := \pi')$ or $c' = (\pi')$

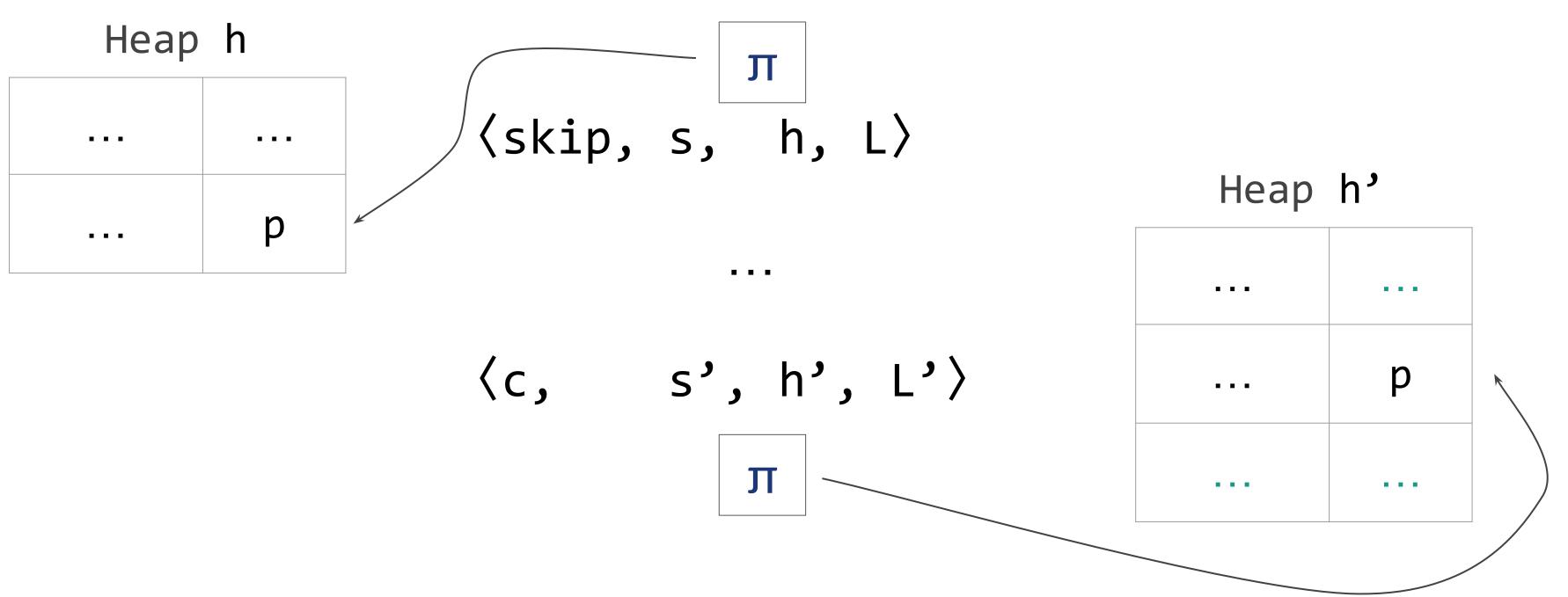
If a path access is recorded in the abstract state, there is a concrete trace exhibiting the access

;
$$i' := x$$
), and $\{\pi\} = \{\pi'\}^{\bar{\theta}}$.





Stable path preserves memory location







Static Analysis

operates in abstract domain

enjoys benefits of the abstraction

Does not need traces $\llbracket C; c \rrbracket^{\#} \langle W, L, A \rangle$

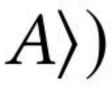
Compositional

 $\llbracket C; c \rrbracket^{\#} \langle W, L, A \rangle = \llbracket c \rrbracket^{\#} (\llbracket C \rrbracket^{\#} \langle W, L, A \rangle)$

• Complete wrt. abstraction

 $\llbracket C \rrbracket^{\#} \perp_{\mathcal{D}} = \alpha \left(\llbracket C \rrbracket \langle S, h, 0 \rangle \right)$

Galois connection







True Positives Theorem

 $\llbracket C_1 \rrbracket^{\#} \perp_{\mathcal{D}}$

access the same path π in WIIW or WIIR or RIIW and π is not wobbly

 π refers to the same location in C₁ & C₂ in the initial state, and it still refers to the same location when concurrently accessed

 $\llbracket C_2
rbrace \# \bot_{\mathcal{D}}$

 $\exists \ \varsigma^{\parallel}. \ \exists \ \mathrm{racy} \ au^{\parallel} \in \llbracket C_1 \ \parallel C_2
rbracket^{\parallel} arsigma$



What is the price to pay for having the TP Theorem?

(Reporting no bugs whatsoever is TP-Sound)

Evaluation

RacerD vs RacerDX

Target	LOC	D CPU	DX CPU	CPU ±%	D Reps	DX Reps	Reps ±%
avrora	76k	103	102	0.4%	143	92	36%
Chronicle-Map	45k	196	196	0.1%	2	2	0%
jvm-tools	33k	106	109	-3.6%	30	26	13%
RxJava	273k	76	69	9.2%	166	134	19%
sunflow	25k	44	44	-1.4%	97	42	57%
xalan-j	175k	144	137	5.0%	326	295	10%

-

-

RacerD vs RacerDX

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The Artifact

At Glance

Contents of Artifact

- Data: 6 packages source code
- Facebook's Infer package (OCaml code, Git repo): holds RacerD
- RacerDX Patch
- Set of Bash scripts to:
 - clean up \bigcirc
 - run vanilla Infer \bigcirc
 - patch and ran patched Infer, \bigcirc
 - collect stats. \bigcirc
- README: dependencies, entry points to run scripts, etc.

6 packages (incl. 2 invalid) by Build Technology

- Ant: 3 pkgs (avrora, sunflow, xalan-j)
- Gradle: 1 pkg (RxJava)
- Maven: 2 pkgs (Chronicle-Map, jvm-tools) the invalid ones

Reproduction & reanalysis

War Stories

Repetition

- First try failed with too new Java (noted in README):
 - error: as of release 9, '_' is a keyword, and may not be used as an identifier
- Second try failed: unrecognized parameter to cloc (not noted in the README).
- Third try partial success: numbers for RacerD are slightly off ???
 - Reason: Missing native dependency
- Finally, numbers for 4 packages did reproduce.

What's Wrong with Maven?

Authors' words (README)

Since submission of the paper for review, the sources of two of the projects (*ChronicLe-Map* and *jvm-tools*) we used for evaluation became uncompilable (due to how maven works -- it **always downloads dependencies from the internet**, and it seems the newer versions are breaking the build of the version we originally tested).

Dependencies? This should have to do with the **build scripts**!

pom.xml of Chronicle-map

20	sur eri accias cili cili cili apistar eri accias	
29	<version>3.16.0</version>	
30	<name>OpenHFT/Chronicle-Map</name>	
31	<description>Chronicle-Map</description>	
32	<packaging>bundle</packaging>	
33		
34	<pre><dependencymanagement> Release</dependencymanagement></pre>	
35	<dependencies></dependencies>	
36		
37	<dependency></dependency>	
38	<groupid>net.openhft</groupid>	
39	<artifactid>third-party-bom</artifactid>	
40	<type>pom</type>	
41	<version>3.6.5</version>	
42	<scope>import</scope>	
43		
44		
45	<dependency></dependency>	
46	<groupid>net.openhft</groupid>	
47	<artifactid>chronicle-bom</artifactid>	
48	<version>1.16.134</version>	
49	<type>pom</type>	
50	<scope>import</scope>	

Remember: "always downloads dependencies from the internet"...

pom.xml of jvm-tools(l)

<pre>1 <project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3.org/2
001/XMLSchema-instance" xsi:schemalocation="http://maven.apache.org/POM/4.0.0 htt
p://maven.apache.org/maven-v4_0_0.xsd"> 2 3 <!--</pre--></project></pre>	<pre>1 <?xml version="1.0" encoding="UTF-8"?> 2 <project xmlns="http://maven.apache.org/POM/4.0.0" xmlns:xsi="http://www.w3 001/XMLSchema-instance" xsi:schemalocation="http://maven.apache.org/POM/4.0 p://maven.apache.org/maven-v4_0_0.xsd"> 3</project></pre>
4 5 Copyright 2012 Alexey Ragozin	4
<pre>6 666971ght For Arekey Ragorin 6 Licensed under the Apache License, Version 2.0 (the "License"); 7 you may not use this file except in compliance with the License. 9 You may obtain a copy of the License at 10 http://www.apache.org/licenses/LICENSE-2.0 12 Unless required by applicable law or agreed to in writing, software 14 distributed under the License is distributed on an "AS IS" BASIS, 15 WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. 16 See the License for the specific language governing permissions and 17 limitations under the License. 18></pre>	
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pom.xml of jvm-tools(II)

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219	cutable>
220	
221	
222	
223	
224	



Infer's bug in management of pom.xml

Error message like the one in the artifact (when enabling stderr):

> Error while running epilogue restoring Maven's pom.xml to its original state: (Unix.Unix_error "No such file or directory" rename "((src /data/videoRecorder/videoRecorder-rpm/pom.xml.infer-orig) (dst /data/videoRecorder/videoRecorder-rpm/pom.xml))").

<u>There's a fix</u> also!

facebook / infer

<> Code

(!) Issues 213

17 Pull requests 6 O Actions

The Fix

[epilogues] do not rely on `at_exit`

Summary:

times. I could not figure out why.

This diff fixes that, but also has more explainable benefits: - Do not run epilogues registered in the parent in the children. Previously it would do so, but probably only if the children registered some epilogue given that `at_exit` must be called again once on the child (but the value of the ref in `Pervasives` would not have been reset).

We already have all the control needed to run epilogues when needed: we know when infer exits, and we know when children processes exit.

Reviewed By: mbouaziz

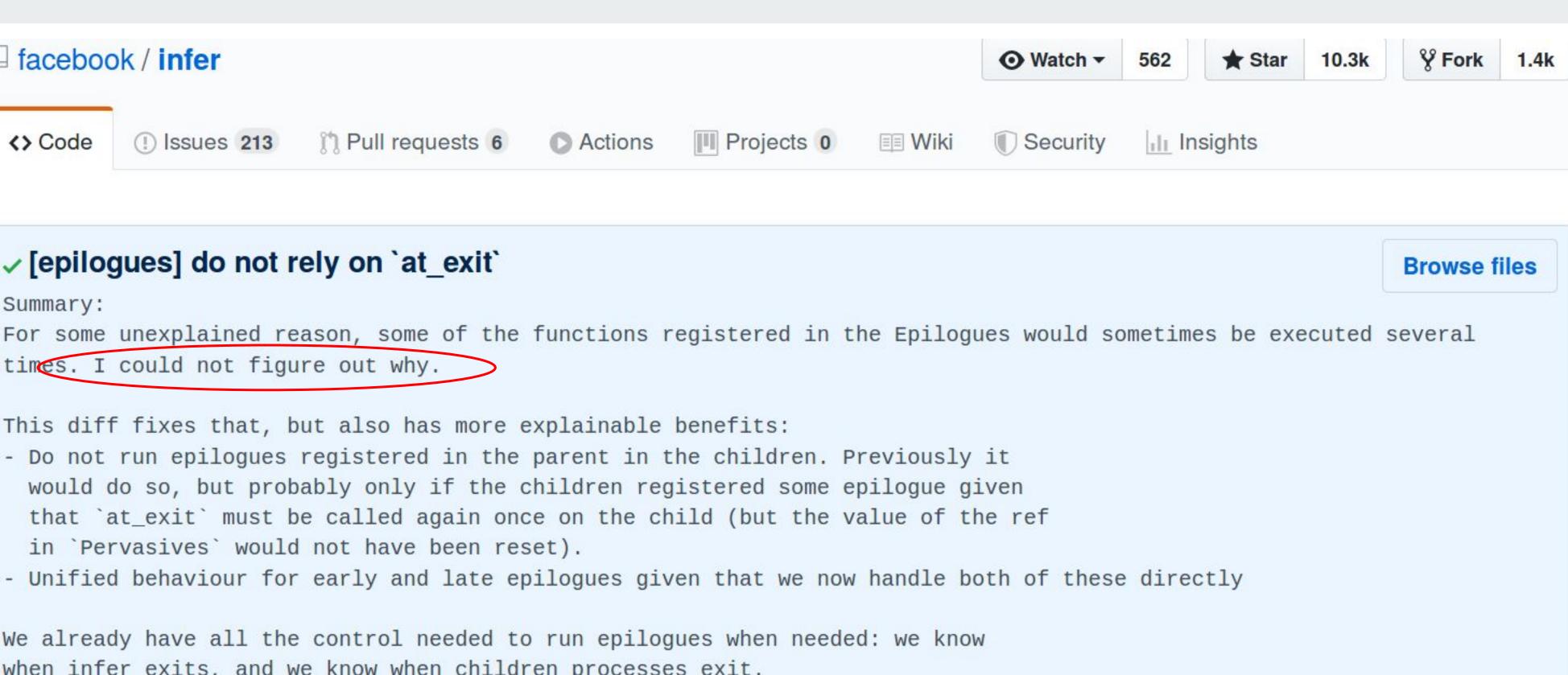
Differential Revision: D9752046

fbshipit-source-id: 13af40081

p master 🚫 v0.17.0 v0.16.0

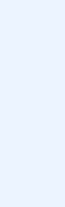


jvillard authored and facebook-github-bot committed on Sep 12, 2018



1 parent 4ddbc71 commit 817f83972cacba03bd9613979fd55154a6b95e20









Happy End with Repetition

After

- Applying the Infer fix (kudos to the authors for preserving the Git repo)
- Checking out released versions of Maven-based packages

We were able to get the numbers from the paper.

Our Experiments (mostly support the claims)

- Full aws-sdk-java died with disk overflow (hundreds of gigabytes of reports) Just one module (aws-java-sdk-s3): \bigcirc \bigcirc
- spring-kafka success, equal results: \bigcirc
- azkaban success, equal, zeroes:

test-azkaban, 76'156, 0, 0, 0, 0 \bigcirc

test-aws-sdk-java, 3'847'035, 666, 639, 64, 48

test-spring-kafka, 30'461, 31, 31, 16, 16

```
"bug class": "PROVER",
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 .\n Reporting because another access to the same memory occurs on a background thread, although this access may not.",
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```

"qualifier": "Unprotected write. Non-private method `void EmbeddedKafkaBroker.afterPropertiesSet()` writes to field `this.org.springframework.kafka.test.EmbeddedKafkaBroker.zkConnect` outside of synchronization

Race Report Example

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                                                                                           boardAndBar() indirectly reads with synchronization from 'this.avrora.gui.Graph
  Numbers.privateNumbers.cck.stat.Sequence.total'. Potentially races with unsynchr
                                                                                           Numbers.privateNumbers.cck.stat.Sequence.total`. Potentially races with unsynchr
                                                                                           onized write in method 'GraphNumbers.internalUpdate()'.\n Reporting because anot
  onized write in method 'GraphNumbers.internalUpdate()'.\n Reporting because anot
  her access to the same memory occurs on a background thread, although this acces
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```



Takeaways: How NOT To Make an Artifact

- No Environment Management (e.g. a VM, Docker, Nix, etc.):
 - a. a bunch of source codes (sometimes non-released versions; not tracked by a VCS, e.g. Git)
 - b. (lose) description of dependencies (some dependencies didn't have corresponding versions, e.g. cloc)
 - c. No way to account for transitive deps of tools, esp. native deps (e.g. sqlite3-dev)
- Clearing the \$PATH -- poor man's env management
- Piping stdout and stderr (!!!) to /dev/null